

Overview of the software architecture for the J-PET tomography device



Wojciech Krzemień

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**INNOWACYJNA
GOSPODARKA**

NARODOWA STRATEGIA SPÓJNOŚCI



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JAGIELLOŃSKI
W KRAKOWIE



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J-PET software

Development of the software that covers
the full data flow
(from hardware to viewer)

- PET image reconstruction and visualization are highly time- and resource-consuming processes
- Additional flexibility required at the development stage (different algorithms, different approaches tested in parallel)

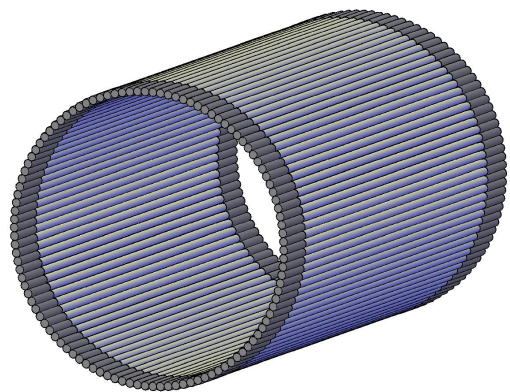
J-PET software

Development of the software that covers
the full data flow
(from hardware to viewer)

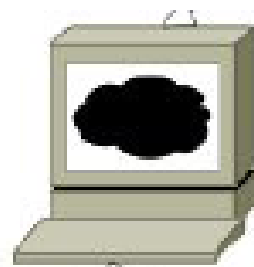
We need:

- data storage, dedicated computing resources
- high-bandwidth transfer links
- efficient algorithms (**parallelization**)
- Other issues e.g. data security

Computing and data flow I



PET Detector

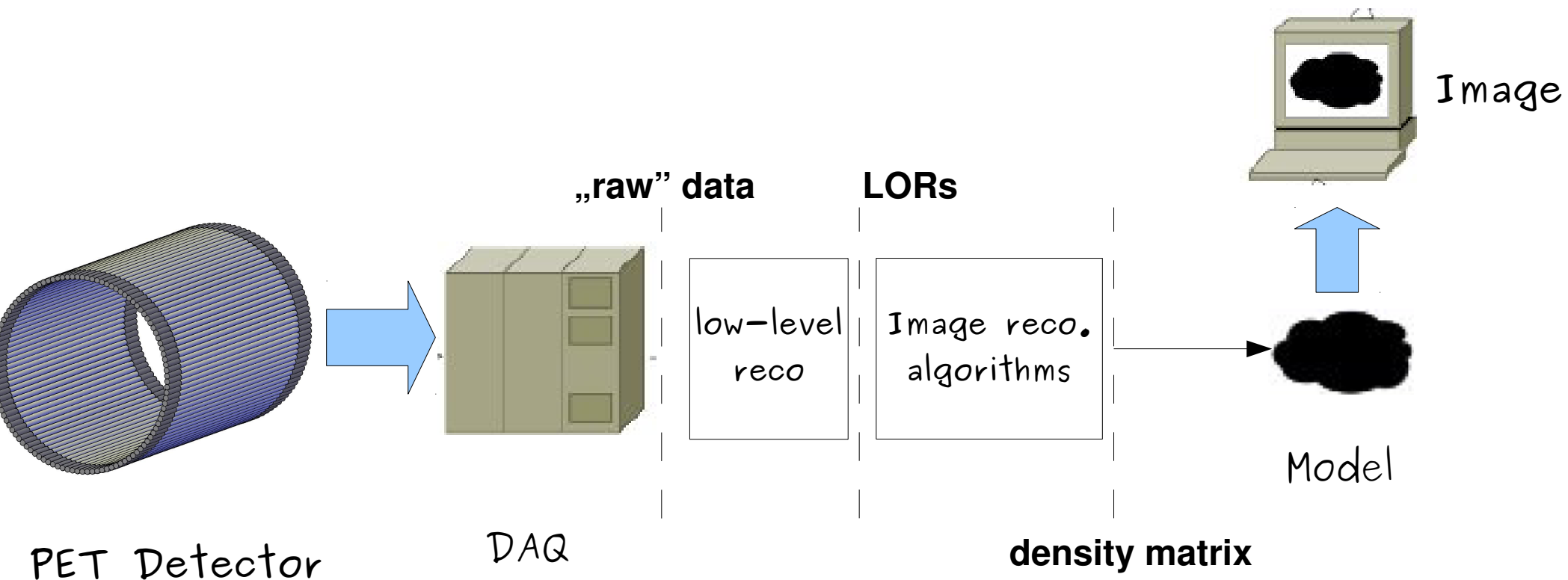


Image



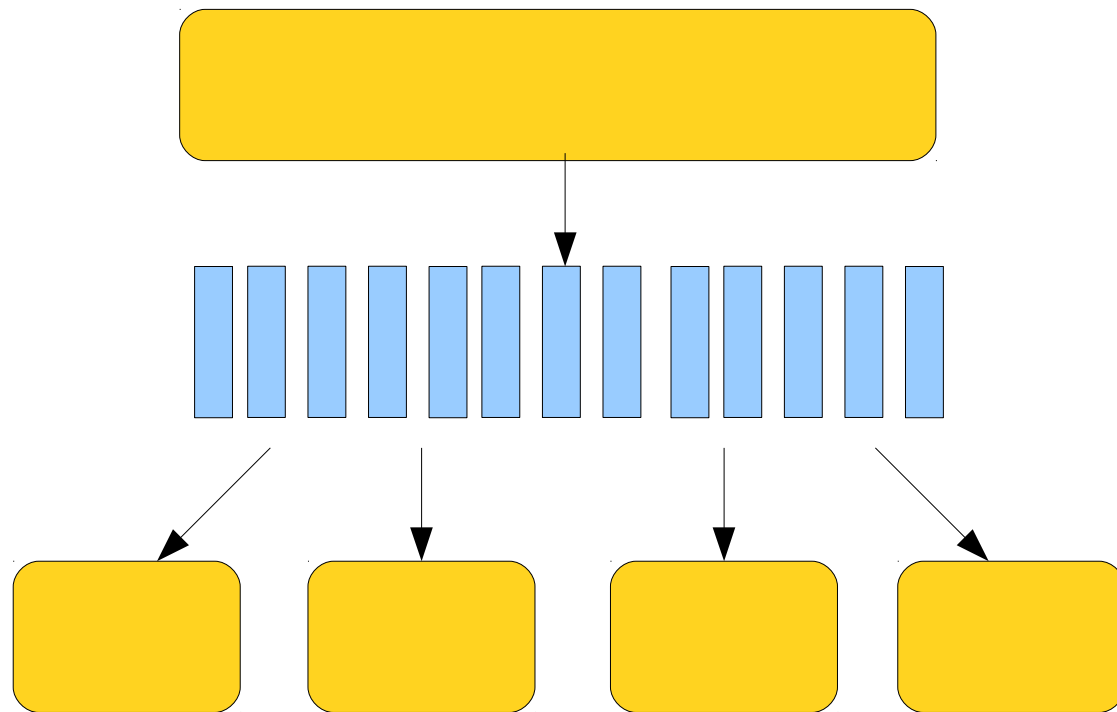
Physician

Computing and data flow II

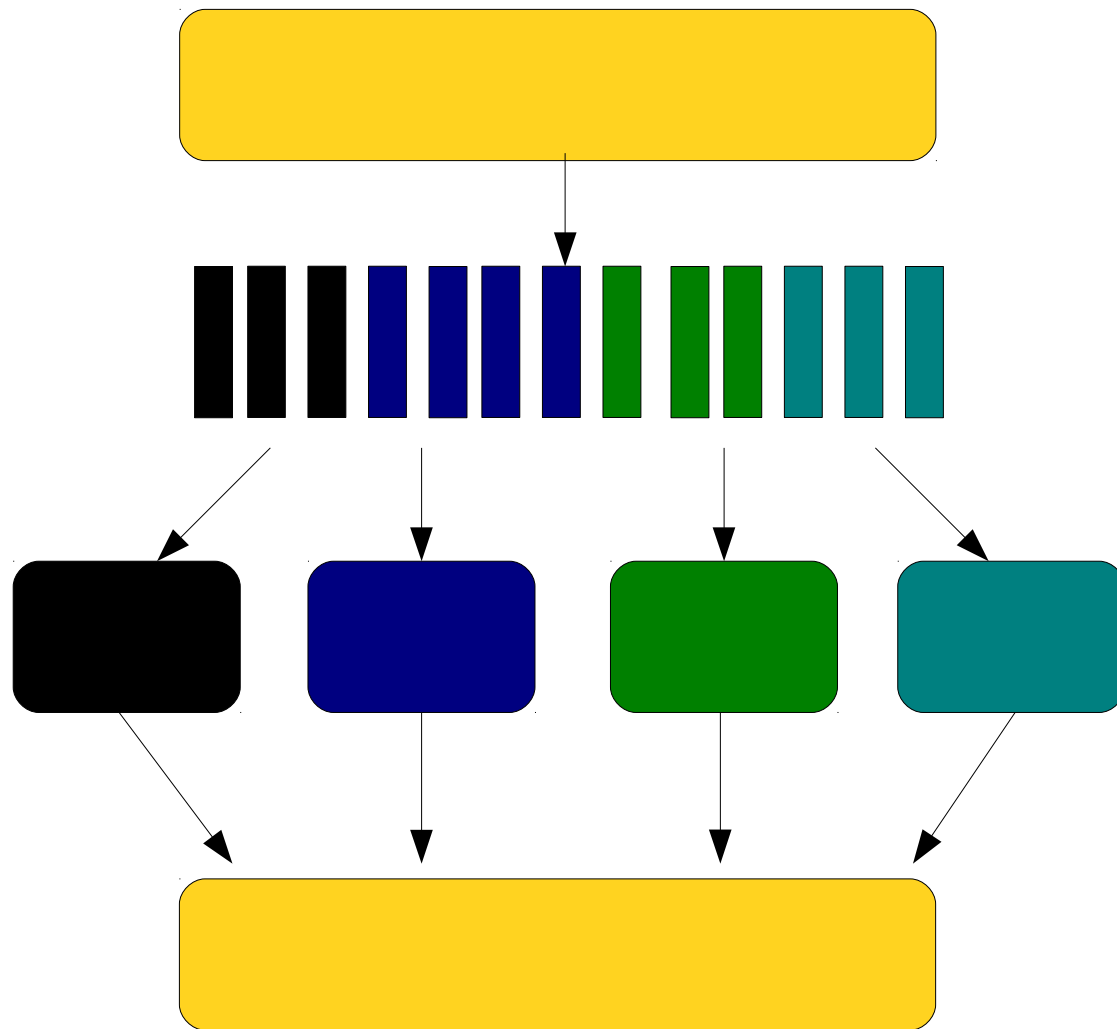


- LOR = Line-of-response
- Low level reconstruction e.g. hit position reconstruction, line-of-response reconstruction, but also some calibration procedures

Parallelization

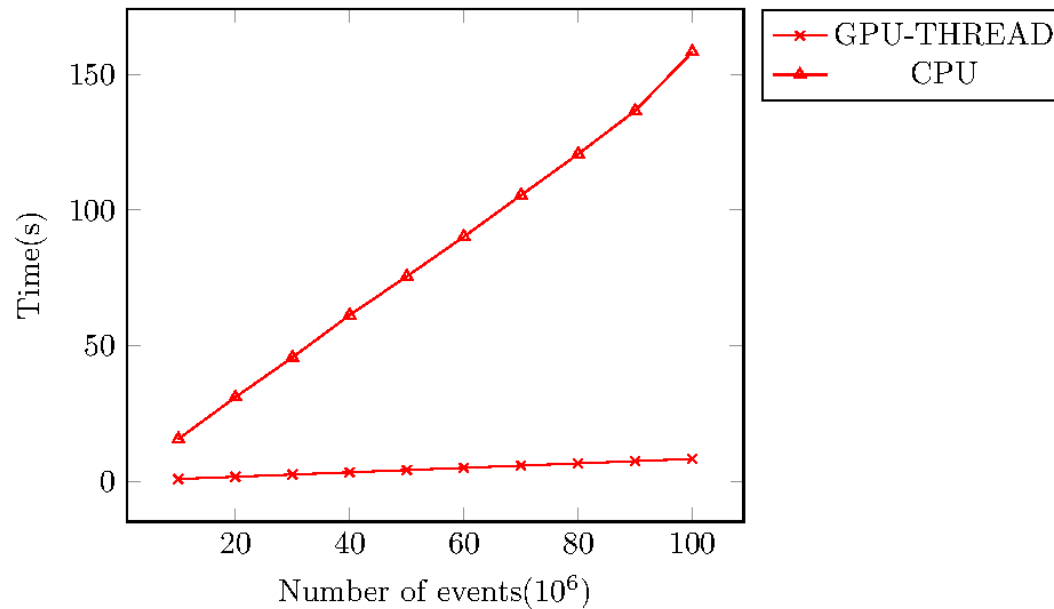


Parallelization



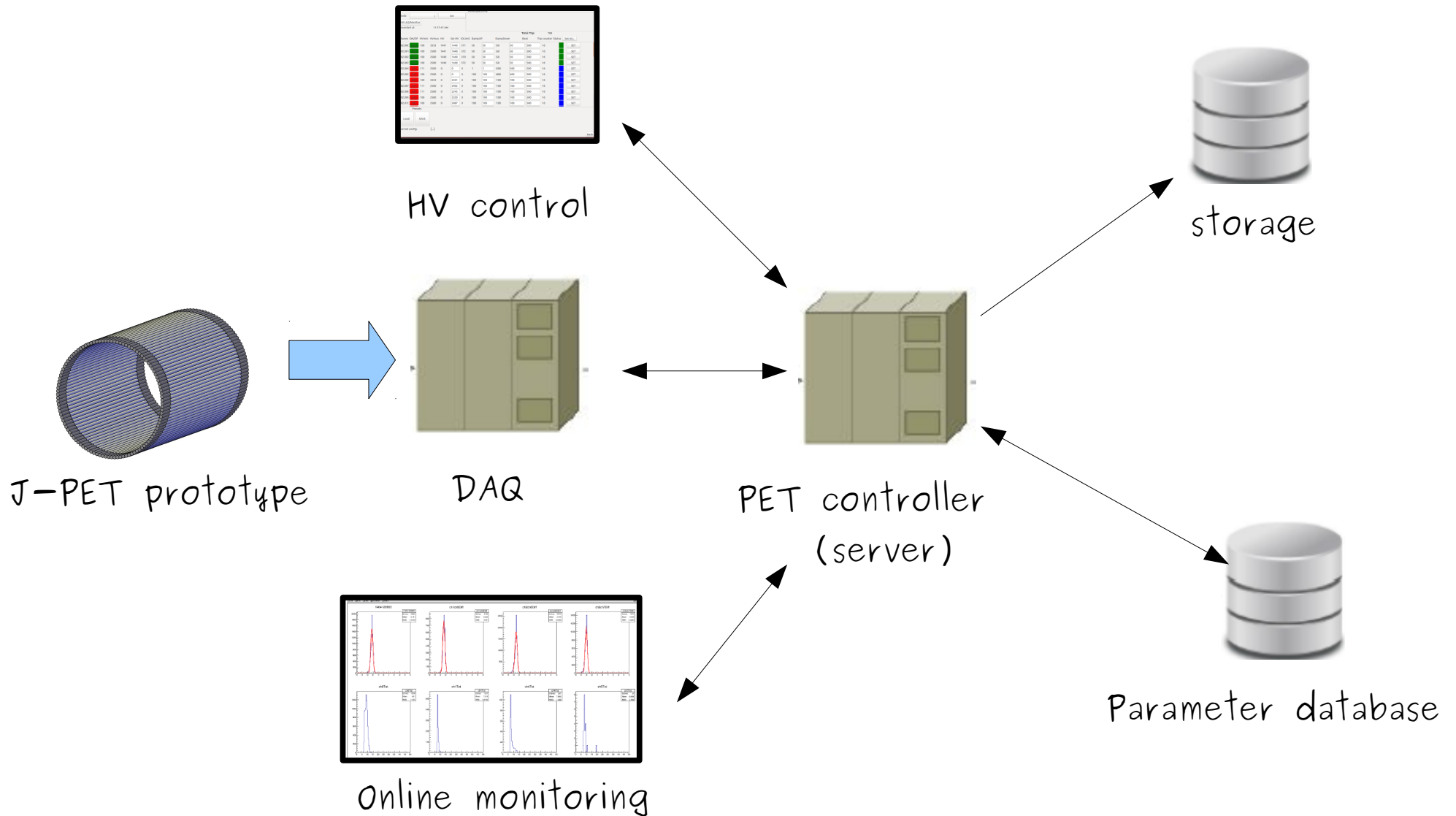
Parallelization

- CPU (Central Processing Unit)
- GPU (Graphics Processing Unit)
- FPGA (Field Programmable Gate Array)



Comparison between CPU and GPU reconstruction time per iteration for example Shepp-Logan phantom

Pet controller - steering and monitoring



In coordination with silvermedia company

PetController - Interface example

PET Controller v.1.0.5281.26059

Setup name: Copy of SetupX II | Description: description | LV Power Supply: LowVX II

High voltage power supply

First power supply: [Edit]

HV Channel | Assigned PM

Board 1, ch 1	[.]
Board 1, ch 2	[.]
Board 1, ch 3	PM0001
Board 1, ch 4	[.]
Board 2, ch 5	PM0004
Board 2, ch 6	PM0009
Board 2, ch 7	[.]
Board 3, ch 7	PM0008
Board 3, ch 8	[Broken]
Board 4, ch 0	[.]
Board 4, ch 1	[.]
Board 4, ch 2	[.]
Board 99, ch 0	[.]

Frame

Very basic made from: [Edit]

Left PM | Scintillator | Right PM

PM0001 | PM0004 | PM0008 | PM0009

Konrad Boards

KB input | KB output

1 -> PM0006 | KB0001 | 1 -> TRB000... | 2 -> TRB000... | 3 | 4

2 | KB0008 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8

TRBs

TRB input | TRB output

2 -> KB000... | TRB0006 | 1 -> Virtual Inpu... | 2 -> Virtual Inpu... | 3 -> Virtual Inpu...

TRB Output Mapping Board

Remove All TOMB Connections

TOMB input

TRB0006.out2 -> Virtual Inpu...
TRB0006.out1 -> Virtual Inpu...
TRB0006.out3 -> Virtual Inpu...

Pending actions:

Code: [Set]

HV List/Monitor

Commanded at: 11:57:47 AM

Total Trip: 768

Channel	ON/OFF	HVmin	HVmax	HV	Set HV	IOLimit	RampUP	RampDown	I0set	Trip counter	Status	Set ALL
02.000	[ON]	100	3333	1441	1440	371	50	50	50	500	16	[SET]
02.001	[ON]	100	2500	1441	1440	372	50	50	50	500	16	[SET]
02.002	[ON]	100	2500	1440	1440	370	50	50	50	500	16	[SET]
02.003	[ON]	100	2500	1440	1440	372	50	50	50	500	16	[SET]
02.004	[OFF]	111	2500	0	0	0	1	1	500	500	16	[SET]
02.005	[OFF]	100	2500	0	0	0	100	100	400	400	16	[SET]
02.006	[OFF]	100	3333	0	2261	0	100	100	100	500	16	[SET]
02.007	[OFF]	111	2500	0	2402	0	100	100	100	500	16	[SET]
02.008	[OFF]	111	2500	0	2245	0	100	100	100	500	16	[SET]
02.009	[OFF]	100	2500	0	2320	0	100	100	100	500	16	[SET]
02.010	[OFF]	100	2500	0	2487	0	100	100	100	500	16	[SET]

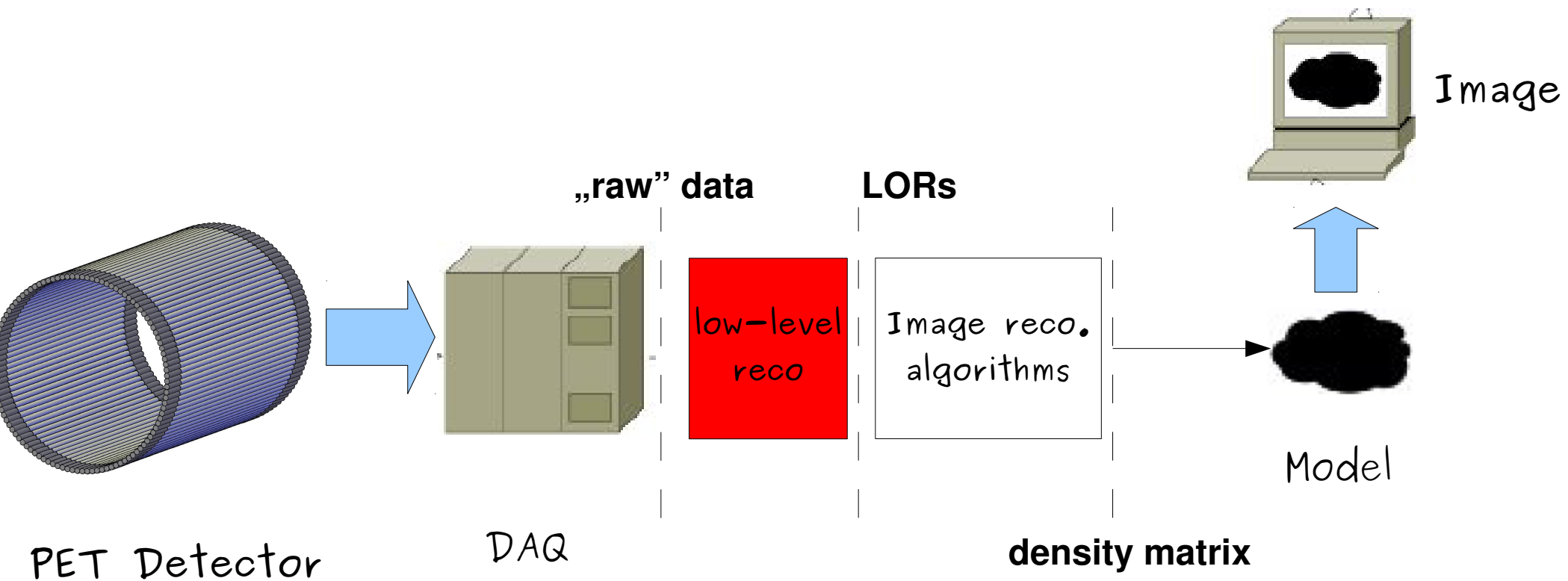
Presets

Load | SAVE

Current config: [...]

Back

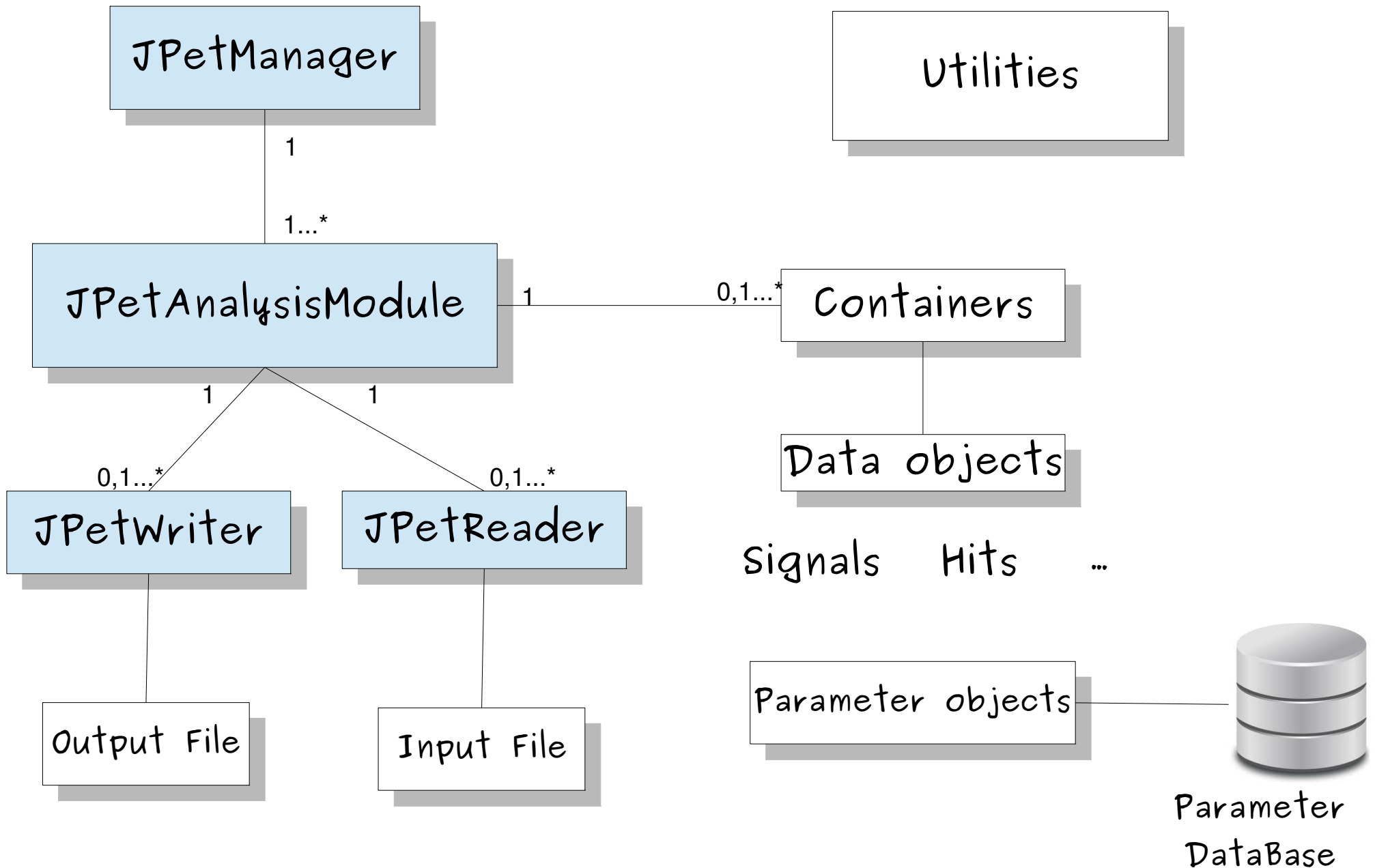
Computing and data flow II



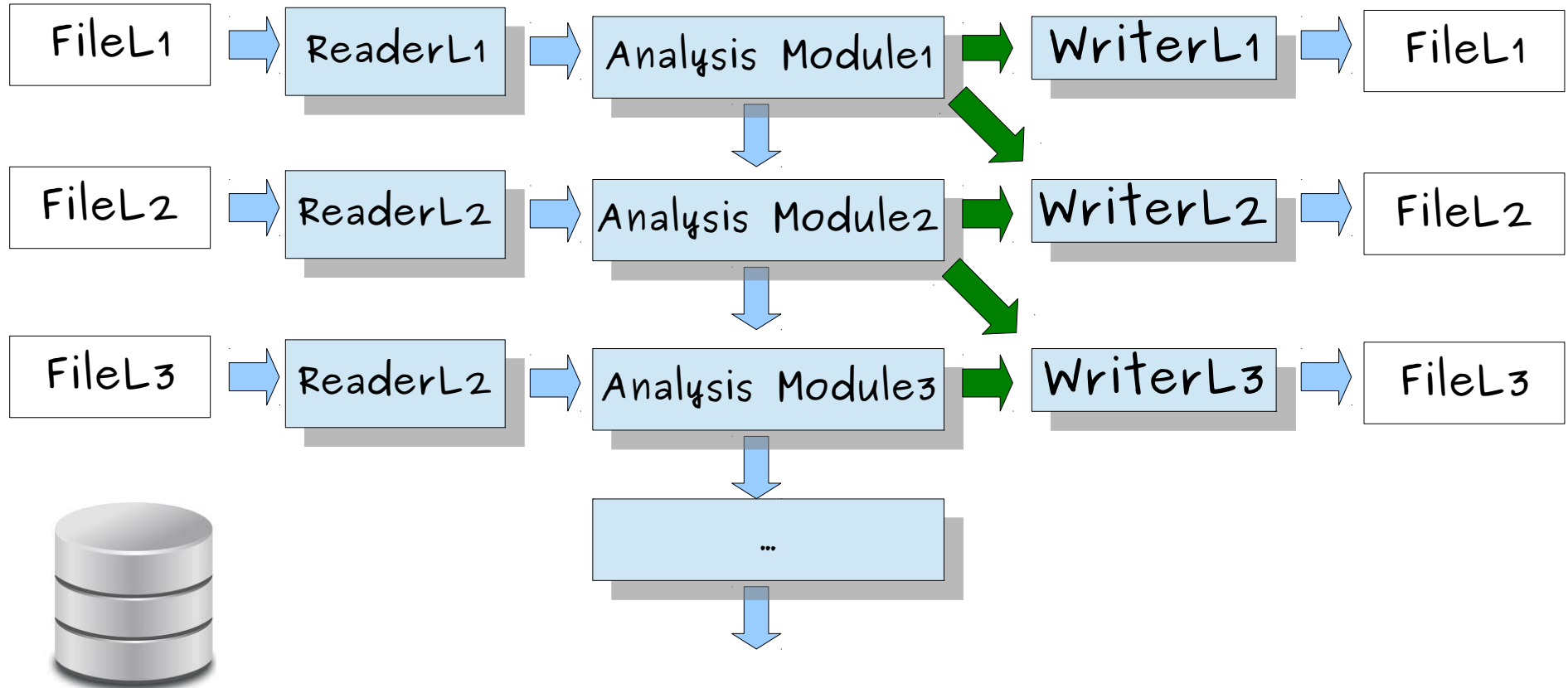
Reconstruction framework

- Software environment for implementation of low-level reconstruction and calibration procedures
- Objectives:
 - Provide a set of software tools,
 - Define common methods and techniques for I/O operations,
 - Handle (hide) the communication with databases
- Technologies & tools: C++, BOOST, git, Doxygen, ROOT

General architecture



Data flow



Parameter
DataBase

Analysis Module e.g.:

- Matching procedure
- Reconstruction procedure
- Calibration procedure

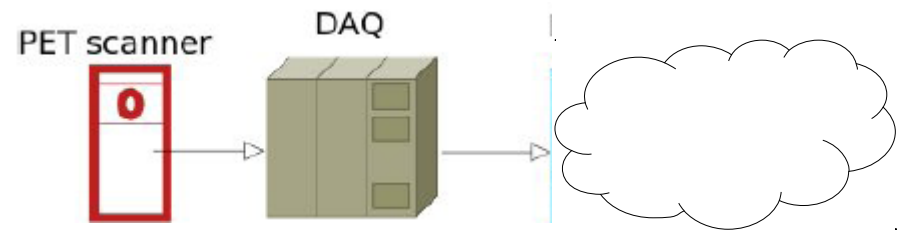
General computing architecture

- In-place processing:

- CPUs
- GPUs
- FPGAs

- Distant processing:

- Cloud
- Grid

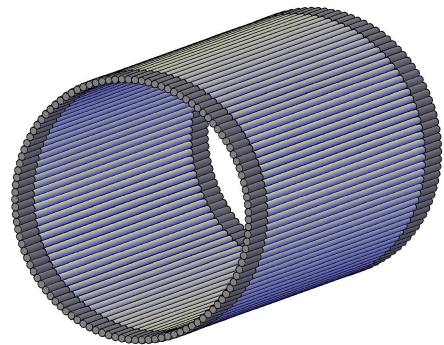


- Data security issues (medical data)

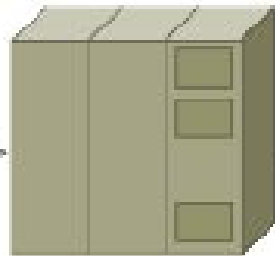
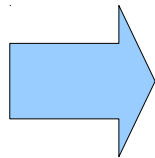
Outlook

- The overall software architecture that covers the whole data flow is being developed by the J-Pet collaboration
- Parallelization techniques are applied to reduce the computing time
- Several computing models are considered to use

Next talks



J-PET Detector

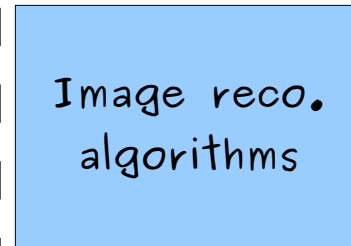


DAQ

„raw” data

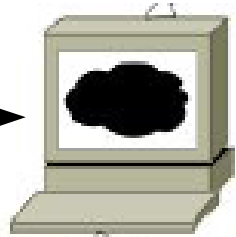


LORs



density matrix

Image



Talks of:
P. Moskal
S. Niedzwiecki
A. Wieczorek &
Ł. Kapton

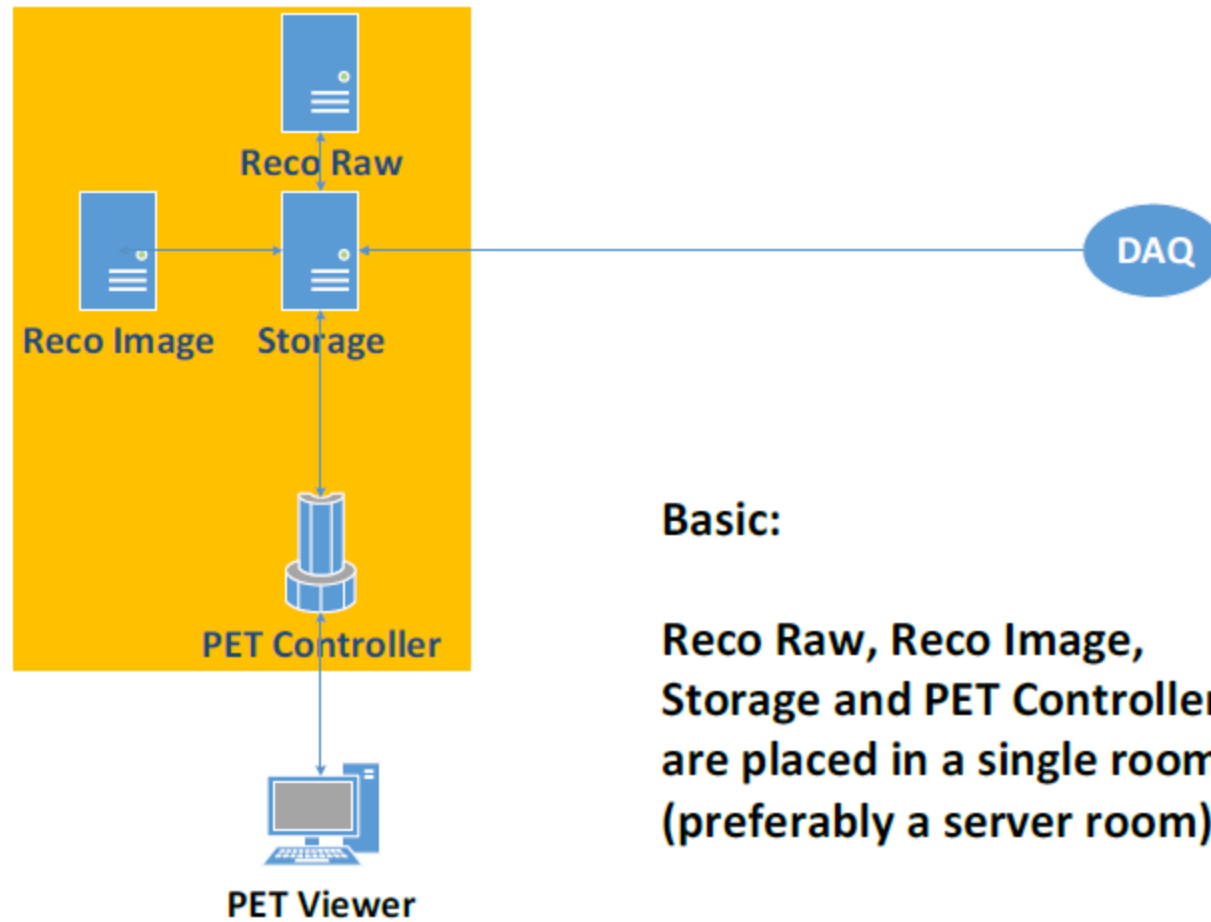
Talks of:
P. Strzempek
G. Korcyl

P. Kowalski – MC
simulations in GATE

Talks of:
L. Raczyński
N. Zoń &
N. Sharma
T. Bednarski

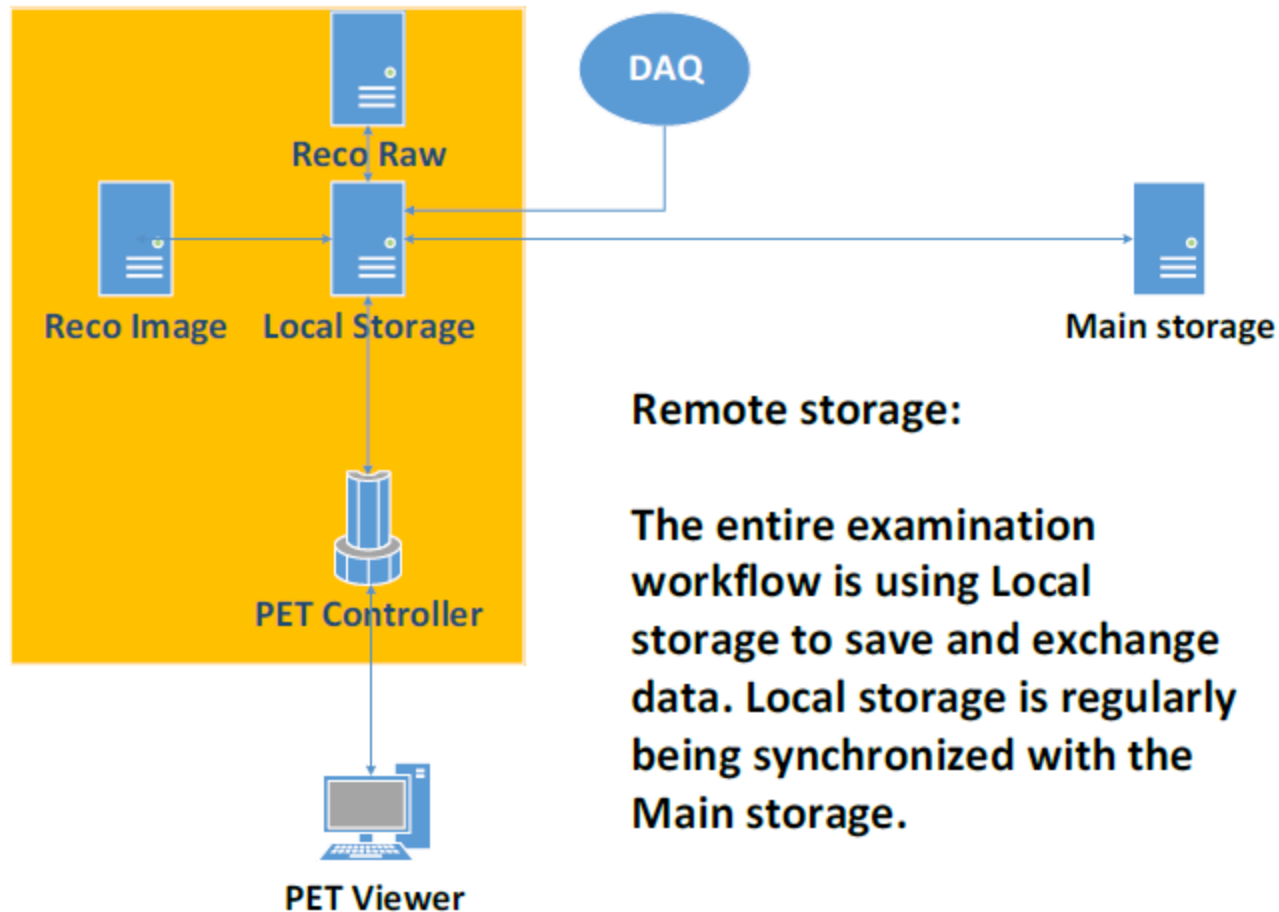
Talks of:
P. Biatas
A. Strzelecki
& J. Kowal

Computing architecture I



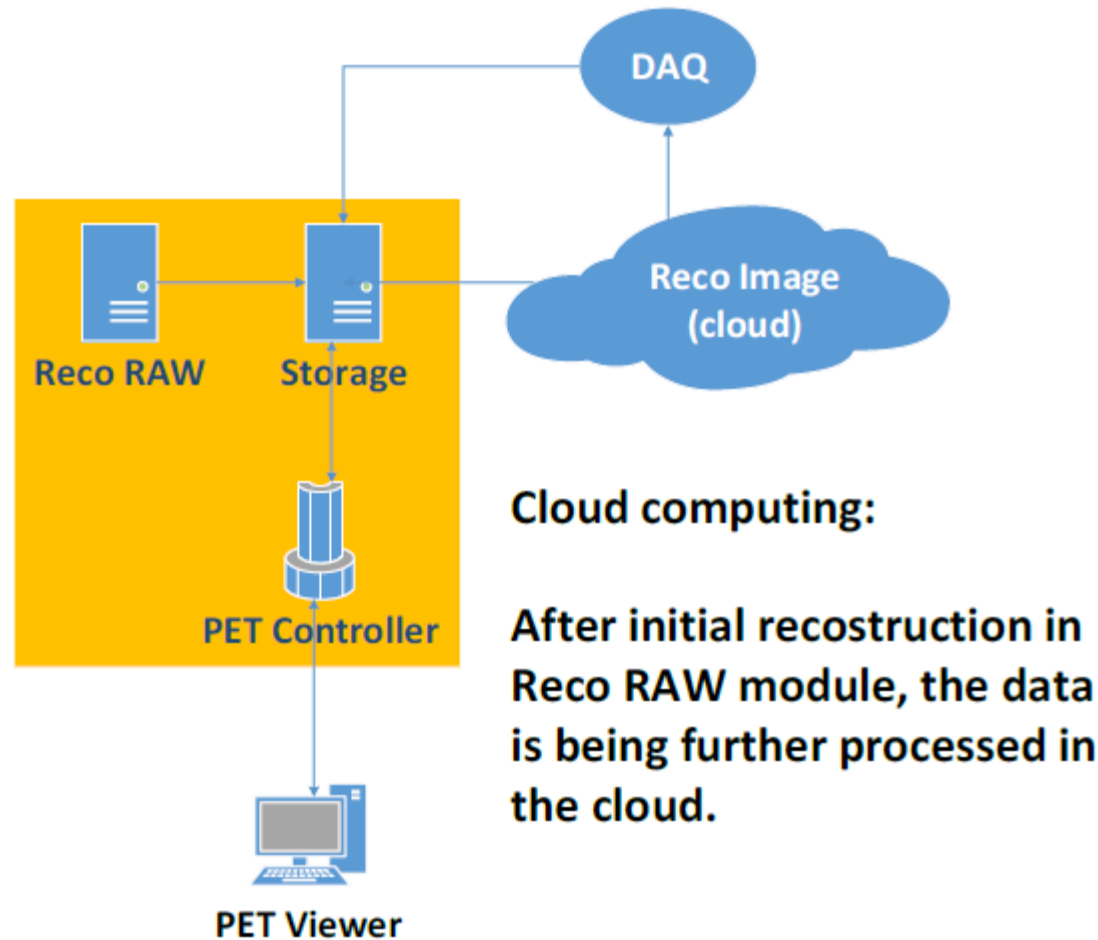
Cooperation with silvermedia

Computing architecture II



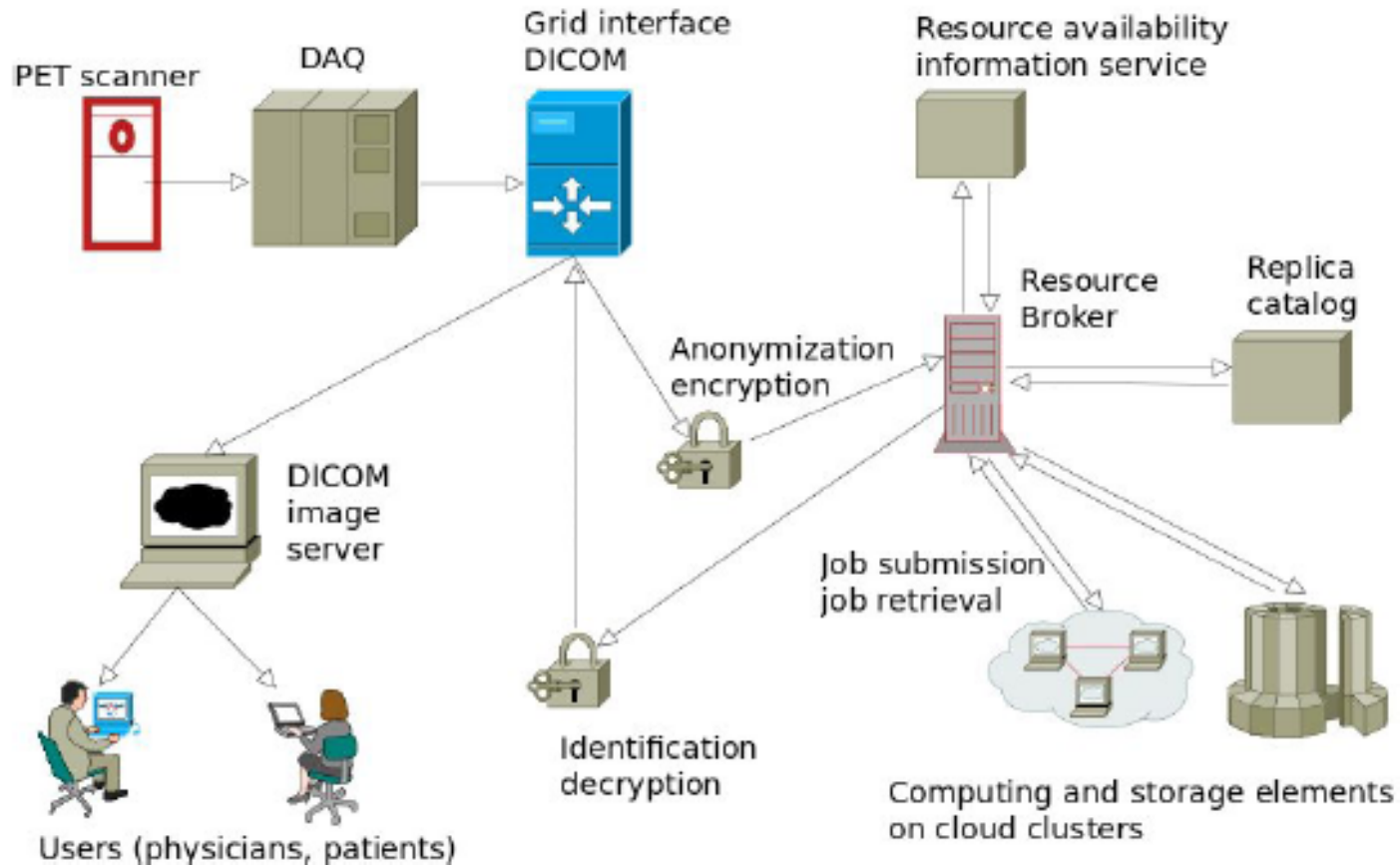
Cooperation with silvermedia

Computing architecture III



Cooperation with silvermedia

GRID-based computing architecture



Picture taken from W. Wiślicki et al. Bio-Algorithms and Med-Systems 10(2014)